

California Institute of Technology
Department of Computer Science
Electronic Design Automation

CS137a, Winter 2004

Administrivia

Monday, January 5

Course Number: CS137a
Course Name: Electronic Design Automation
Term: Winter 2004
Time: MW 9:00AM–10:30AM
Place: Jorgensen 84
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URL: <<http://www.cs.caltech.edu/courses/cs137/>>

Student Requirements and Grading:

Grading is based on participation, assignments, a mini-project, and an end-of-term exam. (approximate weighting shown in brackets):

- Participation (reading, class) [10%]
- Assignments 1 and 2 [15% each]
- Assignment 3 and 4 (project) [40% total]
- Assignment 5 [20%]

Reading and Text Students are expected to read roughly one paper per lecture. Citations for additional reading material will be posted on the web along with the detailed syllabus. There is no required text as I will be pulling together material from many places. Students wishing a single treatment for reference may checkout *Algorithms for VLSI Design Automation* by Sabih Gerez.

Collaboration Policy Each student is expected to do his/her own work. For the project, you are free (and encouraged) to discuss basic strategies and approaches with your fellow classmates or others, but implementations, analysis, and writeups should always be the work of the individual. If you get advice or insights from others that significantly influenced your work, please acknowledge this in your writeups.

Second Term:

Spring term will be mostly focussed on a larger, student-selected project. Preferably this project will come from the student's research experience, but I have many interesting suggestions for those looking for new problems to tackle. There will be additional lectures on advanced topics as well as topics which seem relevant to projects selected.