

# CS151 Complexity Theory

Lecture 3  
April 11, 2023

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## Relationships between classes

- So far:
  - $L \subseteq P \subseteq PSPACE \subseteq EXP$
- believe all containments strict
- know  $L \subsetneq PSPACE$ ,  $P \subsetneq EXP$
- even before any mention of NP, two **major** unsolved problems:

$L \stackrel{?}{=} P$        $P \stackrel{?}{=} PSPACE$

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## A P-complete problem

- We don't know how to prove  $L \neq P$
- But, can identify problems in **P** *least likely* to be in **L** using **P**-completeness.
- need stronger notion of reduction (why?)

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## A P-complete problem

- logspace reduction**:  $f$  computable by TM that uses  $O(\log n)$  space
  - denoted " $L_1 \leq_L L_2$ "
- If  $L_2$  is **P**-complete, then  $L_2$  in **L** implies  $L = P$  (homework problem)

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## A P-complete problem

- Circuit Value (CVAL)**: given a variable-free Boolean circuit (gates  $\wedge, \vee, \neg, 0, 1$ ), does it output 1?

**Theorem**: CVAL is **P**-complete.

- Proof:
  - already argued in **P**
  - $L$  arbitrary language in **P**, TM  $M$  decides  $L$  in  $n^c$  steps

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## A P-complete problem

- Tableau** (configurations written in an array) for machine  $M$  on input  $w$ :

$w_1/q_s$	$w_2$	...	$w_n$	...	—
$w_1$	$w_2/q_1$	...	$w_n$	...	—
$w_1/q_1$	$a$	...	$w_n$	...	—
	$\vdots$				$\vdots$
$\_ / q_e$	$\_$	...	$\_$	...	$\_$

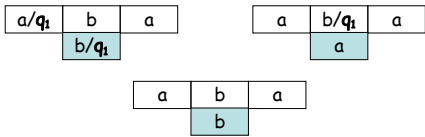
- height = time taken =  $|w|^c$
- width = space used  $\leq |w|^c$

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### A P-complete problem

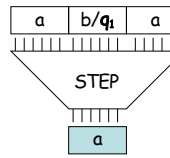
- Important observation: contents of cell in tableau determined by 3 others above it:



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### A P-complete problem

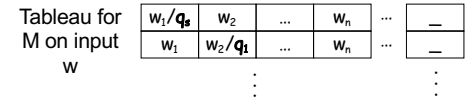
- Can build Boolean circuit STEP
  - input (binary encoding of) 3 cells
  - output (binary encoding of) 1 cell



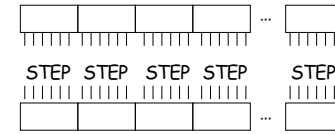
- each output bit is some function of inputs
- can build circuit for each
- size is independent of size of tableau

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### A P-complete problem

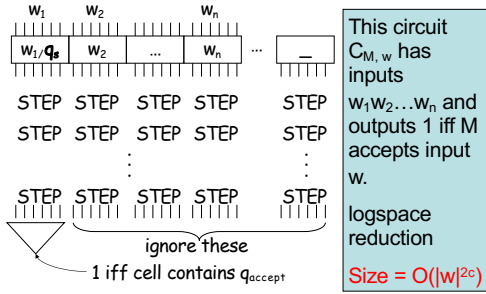


- $|w|^c$  copies of STEP compute row i from i-1



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### A P-complete problem

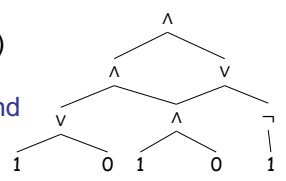


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### Answer to question

- Can we evaluate an n node Boolean circuit using  $O(\log n)$  space?

- **NO!** (probably)
- **CVAL in L if and only if L = P**



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### Padding and succinctness

Two consequences of measuring running time as function of input length:

- “padding”
  - suppose  $L \in \mathbf{EXP}$ , and define  $PAD_L = \{x\#^N : x \in L, N = 2^{|x|^k}\}$
  - TM that decides  $PAD_L$ : ensure suffix of N #s, ignore #s, then simulate TM that decides L
  - running time now polynomial !

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## Padding and succinctness

- converse (intuition only): “succinctness”
  - suppose  $L$  is **P-complete**
  - intuitively, some inputs are “hard” -- require full power of **P**
  - **SUCCINCT $_L$**  has inputs encoded in different form than  $L$ , some exponentially shorter
  - if “hard” inputs are exponentially shorter, then candidate to be **EXP-complete**

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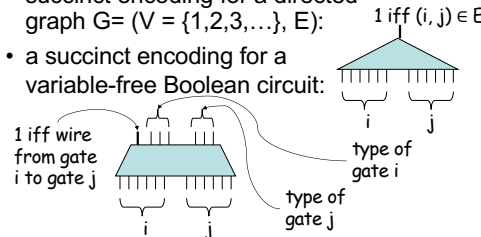
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## Succinct encodings

- succinct encoding for a directed graph  $G = (V = \{1, 2, 3, \dots\}, E)$ :
  - 1 iff  $(i, j) \in E$
- a succinct encoding for a variable-free Boolean circuit:
  - 1 iff wire from gate  $i$  to gate  $j$



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## An EXP-complete problem

- **Succinct Circuit Value**: given a **succinctly encoded** variable-free Boolean circuit (gates  $\wedge, \vee, \neg, 0, 1$ ), does it output 1?
  - **Theorem**: Succinct Circuit Value is **EXP-complete**.
- Proof:
  - in **EXP** (why?)
  - $L$  arbitrary language in **EXP**, TM  $M$  decides  $L$  in  $2^n$  steps

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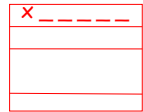
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## An EXP-complete problem

- **tableau** for input  $x = x_1x_2x_3\dots x_n$ :



height,  
width  $2^{n^k}$

- Circuit  $C$  from CVAL reduction has size  $O(2^{2n^k})$ .
- TM  $M$  accepts input  $x$  iff circuit outputs 1

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## An EXP-complete problem

- Can encode  $C$  succinctly:

- 1 iff wire from gate  $i$  to gate  $j$
  - type of gate  $i$
  - type of gate  $j$
- 
- if  $i, j$  within single STEP circuit, easy to compute output
  - if  $i, j$  between two STEP circuits, easy to compute output
  - if one of  $i, j$  refers to input gates, consult  $x$  to compute output

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## Summary

- Remaining TM details: big-oh necessary.
- First complexity classes:
  - **L, P, PSPACE, EXP**
- First separations (via simulation and diagonalization):
  - **P ≠ EXP, L ≠ PSPACE**
- First major open questions:
  - **L ? P**      **P ? PSPACE**
- First complete problems:
  - CVAL is P-complete
  - Succinct CVAL is EXP-complete

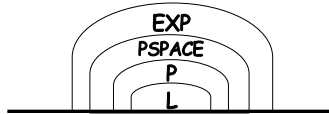
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## Summary



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## Nondeterminism: introduction

A motivating question:

- Can computers replace mathematicians?

$$L = \{ \langle x, 1^k \rangle : \text{statement } x \text{ has a proof of length at most } k \}$$

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## Nondeterminism: introduction

- Outline:
  - nondeterminism
  - nondeterministic time classes
  - **NP**, **NP**-completeness, **P** vs. **NP**
  - **coNP**
  - NTIME Hierarchy
  - Ladner's Theorem

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## Nondeterminism

- Recall deterministic TM
  - $Q$  finite set of states
  - $\Sigma$  alphabet including blank: “\_”
  - $q_{\text{start}}, q_{\text{accept}}, q_{\text{reject}}$  in  $Q$
  - transition function:
 
$$\delta : Q \times \Sigma \rightarrow Q \times \Sigma \times \{L, R, -\}$$

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## Nondeterminism

- **nondeterministic** Turing Machine:
  - $Q$  finite set of states
  - $\Sigma$  alphabet including blank: “\_”
  - $q_{\text{start}}, q_{\text{accept}}, q_{\text{reject}}$  in  $Q$
  - **transition relation**

$$\Delta \subseteq (Q \times \Sigma) \times (Q \times \Sigma \times \{L, R, -\})$$
- given current state and symbol scanned, several choices of what to do next.

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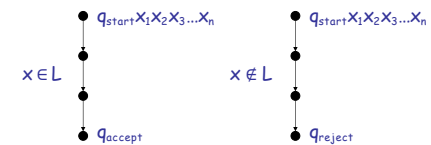
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## Nondeterminism

- deterministic TM: given current configuration, unique next configuration



- nondeterministic TM: given current configuration, several possible next configurations

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### Nondeterminism

$q_{start}x_1x_2x_3\dots x_n$

$x \in L$

“guess”

$q_{accept}$

$q_{start}x_1x_2x_3\dots x_n$

$x \notin L$

“computation path”

$q_{reject}$

- asymmetric accept/reject criterion

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### Nondeterminism

- all paths terminate
- **time used**: maximum length of paths from root
- **space used**: maximum # of work tape squares touched on any path from root

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### Nondeterminism

- **NTIME(f(n))** = languages decidable by a multi-tape NTM that runs for at most f(n) steps *on any computation path*, where n is the input length, and  $f : \mathbf{N} \rightarrow \mathbf{N}$
- **NSPACE(f(n))** = languages decidable by a multi-tape NTM that touches at most f(n) squares of its work tapes *along any computation path*, where n is the input length, and  $f : \mathbf{N} \rightarrow \mathbf{N}$

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### Nondeterminism

- Focus on time classes first:

**$NP = \cup_k NTIME(n^k)$**

**$NEXP = \cup_k NTIME(2^{n^k})$**

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### Poly-time verifiers

Very useful alternative of NP: “witness” or “certificate”

**Theorem:** language L is in NP if & only if efficiently verifiable it is expressible as:

$L = \{x \mid \exists y, |y| \leq |x|^k, (x, y) \in R\}$

where R is a language in P.

- poly-time TM  $M_R$  deciding R is a **“verifier”**

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### Poly-time verifiers

- Example: 3SAT expressible as

$3SAT = \{\varphi \mid \varphi \text{ is a 3-CNF formula for which } \exists \text{ assignment } A \text{ for which } (\varphi, A) \in R\}$

$R = \{(\varphi, A) \mid A \text{ is a sat. assign. for } \varphi\}$

- satisfying assignment A is a “witness” of the satisfiability of  $\varphi$  (“certifies” satisfiability of  $\varphi$ )
- R is decidable in poly-time

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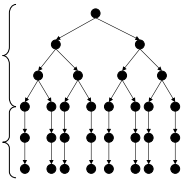
## Poly-time verifiers

$L = \{ x \mid \exists y, |y| \leq |x|^k, (x, y) \in R \}$

**Proof:** ( $\Rightarrow$ ) give poly-time NTM deciding L

phase 1: "guess" y  
with  $|x|^k$   
nondeterministic steps

phase 2:  
decide if  
 $(x, y) \in R$



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## Poly-time verifiers

**Proof:** ( $\Leftarrow$ ) given  $L \in \text{NP}$ , describe L as:  
 $L = \{ x \mid \exists y, |y| \leq |x|^k, (x, y) \in R \}$

- L is decided by NTM M running in time  $n^k$
- define the language  
 $R = \{ (x, y) : y \text{ is an accepting computation history of M on input } x \}$
- check: accepting history has length  $\leq |x|^k$
- check: R is decidable in polynomial time
- check: M accepts x iff  $\exists y, |y| \leq |x|^k, (x, y) \in R$

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## Why NP?

problem requirements

object we are seeking

stochastic model of

- but, captures important computational feature of many problems:

exhaustive search w/

- contains **huge** number of natural problems
- many problems have form:

efficient test: does y meet requirements?

$L = \{ x \mid \exists y \text{ s.t. } (x, y) \in R \}$

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## Why NP?

- Why not **EXP**?
  - too strong!
  - important problems not complete.

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## Relationships between classes

- Easy:  $P \subseteq \text{NP}$ ,  $\text{EXP} \subseteq \text{NEXP}$ 
  - TM special case of NTM
- Recall:  $L \in \text{NP}$  iff expressible as  
 $L = \{ x \mid \exists y, |y| \leq |x|^k \text{ s.t. } (x, y) \in R \}$
- $\text{NP} \subseteq \text{PSPACE}$  (try all possible y)
- The central question:  
 $P \stackrel{?}{=} \text{NP}$   
 finding a solution vs. recognizing a solution

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## NP-completeness

- **Circuit SAT**: given a Boolean circuit (gates  $\wedge, \vee, \neg$ ), with variables  $y_1, y_2, \dots, y_m$  is there some assignment that makes it output 1?

**Theorem:** Circuit SAT is NP-complete.

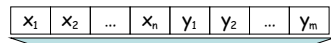
- Proof:
  - clearly in NP

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## NP-completeness

- Given  $L \in \text{NP}$  of form  
 $L = \{x \mid \exists y \text{ s.t. } (x,y) \in R\}$



1 iff  $(x,y) \in R$       CVAL reduction for R

- hardwire input  $x$ ; leave  $y$  as variables

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## NEXP-completeness

- Succinct Circuit SAT:** given a **succinctly encoded** Boolean circuit (gates  $\wedge, \vee, \neg$ ), with variables  $y_1, y_2, \dots, y_m$  is there some assignment that makes it output 1?

**Theorem:** Succinct Circuit SAT is **NEXP**-complete.

- Proof:
  - same trick as for Succinct CVAL **EXP**-complete.

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## Complement classes

- In general, if **C** is a complexity class
- co-C** is the complement class, containing all **complements** of languages in **C**
  - $L \in \mathbf{C}$  implies  $(\Sigma^* - L) \in \mathbf{co-C}$
  - $(\Sigma^* - L) \in \mathbf{C}$  implies  $L \in \mathbf{co-C}$
- Some classes closed under complement:
  - e.g.  $\mathbf{co-P} = \mathbf{P}$

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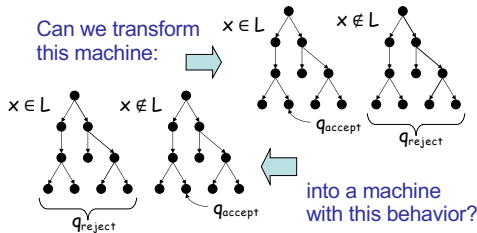
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## coNP

- Is NP closed under complement?

Can we transform this machine:



into a machine with this behavior?

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## coNP

- “proof system” interpretation:
- Recall:  $L \in \text{NP}$  iff expressible as  
 $L = \{x \mid \exists y, |y| \leq |x|^k, (x, y) \in R\}$ 
  - “proof”
  - “proof verifier”
- languages in **NP** have “short proofs”
- coNP** captures (in its complete problems) problems **least likely** to have “short proofs”.
  - e.g., UNSAT is **coNP**-complete

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## coNP

- P = NP** implies **NP = coNP**
- Belief:
  - NP  $\neq$  coNP**
  - another major open problem

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## NTIME Hierarchy Theorem

**Theorem** (Nondeterministic Time Hierarchy Theorem):  
 For every proper complexity function  $f(n) \geq n$ , and  $g(n) = \omega(f(n+1))$ ,

$\text{NTIME}(f(n)) \subsetneq \text{NTIME}(g(n))$ .

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## NTIME Hierarchy Theorem

**Proof attempt:** (what's wrong?)

Turing Machines

D: [ n y n y y n y ]

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## NTIME Hierarchy Theorem

- Let  $t(n)$  be large enough so that can decide if NTM  $M$  running in time  $f(n)$  accepts  $1^n$ , in time  $t(n)$ .

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## NTIME Hierarchy Theorem

- Enough time on input  $1^{t(n)}$  to do the opposite of  $M_i(1^n)$ :

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## NTIME Hierarchy Theorem

- For  $k$  in  $[n \dots t(n)]$  can do same as  $M_i(1^{k+1})$  on input  $1^k$

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## NTIME Hierarchy Theorem

- Did we diagonalize against  $M_i$ ?
  - if  $L(M_i) = L(D)$  then:
    - equality along all arrows.
    - contradiction.

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## NTIME Hierarchy Theorem

- General scheme:
  - interval  $[1 \dots t(1)]$  kills  $M_1$
  - interval  $[t(1) \dots t(t(1))]$  kills  $M_2$
  - interval  $[t^{i-1}(1) \dots t^i(1)]$  kills  $M_i$
- Running time of D on  $1^n$ :  $f(n+1)$  + time to compute interval containing  $n$
- conclude D in **NTIME(g(n))** ( $g(n) = \omega(f(n+1))$ )

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